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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Ki-nam KIM et al.

Art Unit: TBD

Appl. No.: NEW

Examiner: TBD

Filed: 10 March 2004

Atty. Docket: SEC.959D

For: **Semiconductor Memory Device and Method for Manufacturing the Same**

CLAIM OF PRIORITY

U.S. Patent and Trademark Office
2011 South Clark Place
Customer Window, **Mail Stop Patent Application**
Crystal Plaza Two, Lobby, Room 1B03
Arlington, Virginia 22202

Sir:

Applicant, in the above-identified application, hereby claims the priority date
under the International Convention of the following Korean application:

Appln. No. 2002-0002509 filed January 16, 2002

as acknowledged in the Declaration of the subject application.

A certified copy of said application was filed in the parent application.

Respectfully submitted,

VOLENTINE FRANCOS, P.L.L.C.

By: _____



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Date: 10 March 2004



별첨 사본은 아래 출원의 원본과 동일함을 증명함.

This is to certify that the following application annexed hereto is a true copy from the records of the Korean Intellectual Property Office.

출원 번호 : 특허출원 2002년 제 2509 호
Application Number PATENT-2002-0002509

출원 년 월 일 : 2002년 01월 16일
Date of Application JAN 16, 2002

출원인 : 삼성전자 주식회사
Applicant(s) SAMSUNG ELECTRONICS CO., LTD.



2002 년 05 월 06 일

특 허 청

COMMISSIONER

